

GA 2879

Patent

Docket No.: CDST-S118.CIP

Invention Disclosure Statement Transmittal

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner of Patents and Trademarks, Washington, D.C., 20231, on the below date of deposit.			
Date of Deposit:	6/20/00	Name of Person Making the Deposit:	Luz Castillo
		Signature of the Person Making the Deposit:	<i>Luz Castillo</i>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Pan

Serial No.: 09/493,697

Group Art Unit: 2879

Filed: January 28, 2000

Examiner:

Title: TAILORED SPACER WALL COATINGS FOR REDUCED SECONDARY ELECTRON EMISSION

The Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

Information Disclosure Statement Transmittal

Transmitted herewith is the following:

- Formal drawings, totaling sheets.
- Informal drawings, totaling sheets.
- Certification for PTO Consideration
- ☒ Information Disclosure statement (1 sheet)
- Information Disclosure statement and late filing fee
- ☒ Form 1449
- Petition for Extension of Time
- Other:

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Fee Calculation (for other than a small entity)

Fee Items				Fee Rate	Total
Petition for Extension of Time (fee calculated elsewhere)				\$.00	0
Information Disclosure Statement, late filing				\$240.00	0
Other:					
Total Fees					0

PAYMENT OF FEES

1. The full fee due in connection with this communication is provided as follows:
 - [] The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.
A duplicate copy of this authorization is enclosed.
 - [] A check in the amount of \$
 - [X] Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

Please direct all correspondence concerning the above-identified application to the following address:



WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060

Respectfully submitted,

Date: 6/20/00

By: [Signature]

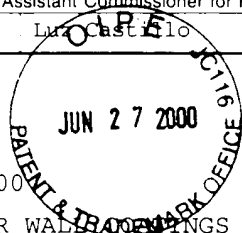
John P. Wagner, Jr.
Reg. No. 35, 398

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Docket No.: CDST-S118.CIP

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Assistant Commissioner for Patents, Washington, D.C., 20231, on the below date of deposit.			
Date of Deposit:	Name of Person Making the Deposit:	Inventor(s):	Signature of the Person Making the Deposit:
6/28/00	L. J. Castillo	Pan	<i>[Signature]</i>



Inventor(s): Pan

Serial No.: 09/493,697

Filed: January 28, 2000

Title: TAILORED SPACER WALLS FOR REDUCED SECONDARY ELECTRON EMISSION

Group Art Unit: 2879

Examiner:

The Assistant Commissioner for Patents
Washington, D.C. 20231
Sir:

Certification for PTO Consideration of an Information Disclosure Statement
(Under 37 CFR §1.97)

Consideration of the enclosed Information Disclosure Statement is requested.

1. This certification is being made for this information disclosure statement
- ☒ [X] accompanying this certification
- ☐ [] Filed _____

2. I hereby certify that:

- ☒ [X] Each item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the statement.
- ☐ [] No item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart application, or, to the knowledge of the person signing certification after making reasonable inquiry, was known to any individual designated in §1.56(c) more than three months prior to the filing of the statement.

3. The person making this certification is

- ☒ [x] a person who is substantively involved in the preparation or prosecution of the application, and who is associated with the inventor, with the assignee, or with anyone to whom there is an obligation to assign the application (37 C.F.R. 1.56 (c)) and who signs below.
- ☐ [] the inventor(s) who signs below
- ☐ [] the practitioner who signs below on the basis of the information:
- ☐ [] supplied by the inventors
- ☐ [] supplied by an individual designated in § 1.56(c)
- ☐ [] in the practitioners file

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Please direct all correspondence concerning the above-identified application to the following address:



WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060

Respectfully submitted,

Date: _____

6/29/00

By: _____

John P. Wagner, Jr.
Reg. No. 35,398

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: CDST-S118.CIP

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Inventor(s): Pan
Serial No.: 09/493,697
Group Art Unit: 2879
Filed: January 28, 2000
Examiner:
Title: TAILORED SPACER WALL COATINGS FOR REDUCED SECONDARY ELECTRON EMISSION

The Commissioner of Patents and Trademarks
Washington, D.C. 20231
Sir:

Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(b)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(b) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following U.S. Patents:

<u>Pat. No.</u>	<u>Pat. Title</u>	<u>Grant Date</u>
5,614,781	STRUCTURE AND OPERATION OF HIGH VOLTAGE SUPPORTS	Mar. 25, 1997
5,675,212	SPACER STRUCTURES FOR USE IN FLAT PANEL DISPLAYS AND METHODS FOR FORMING SAME	Oct. 7, 1997
5,726,529	SPACER FOR FIELD EMISSION DISPLAY	Mar. 10, 1998
5,872,424	HIGH VOLTAGE COMPATIBLE SPACER COATING	Feb. 16, 1999

Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060

Respectfully submitted,

Date: 6/24/00

By: [Signature]
John P. Wagner, Jr.
Reg. No. 35,398